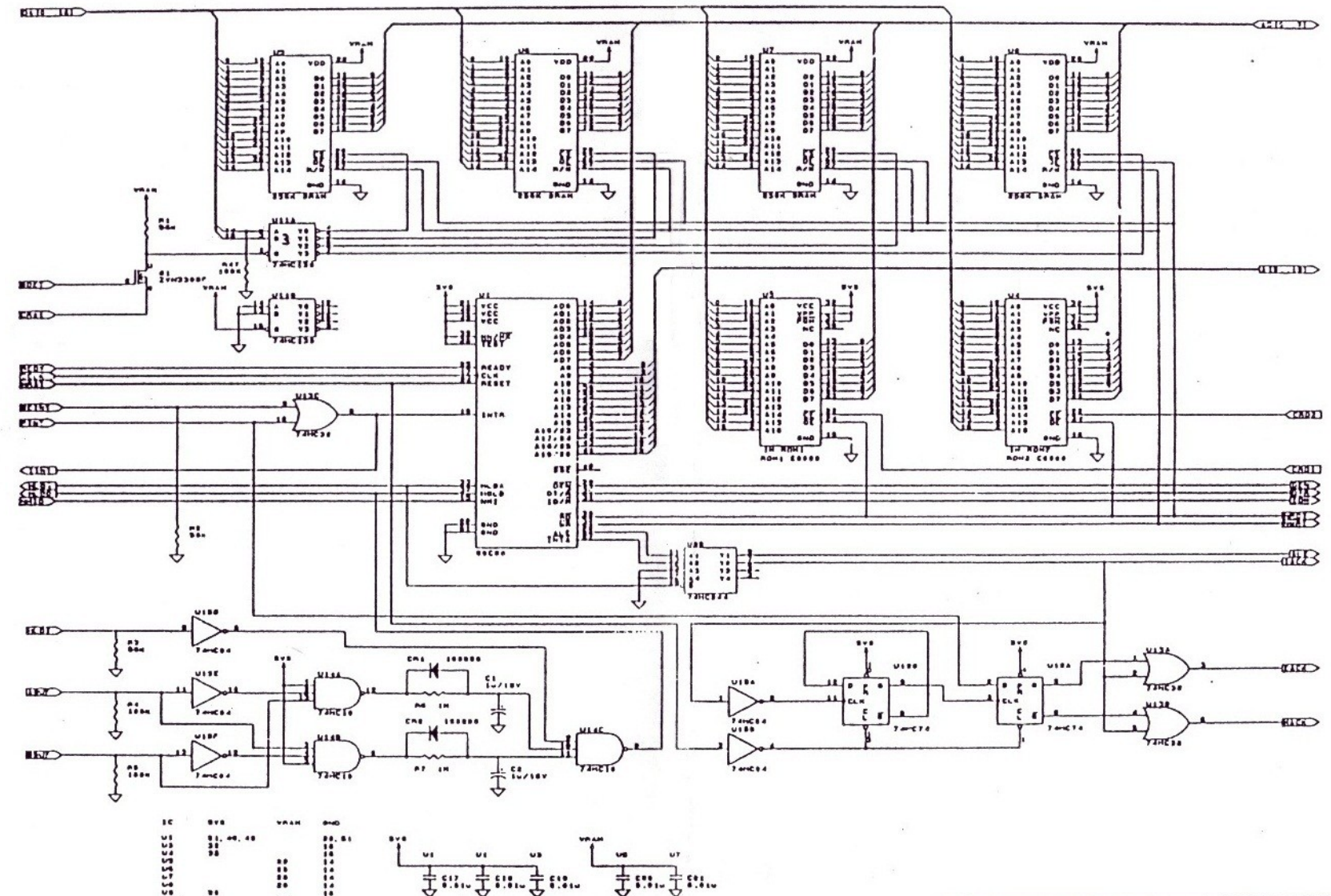


NOTES—UNLESS OTHERWISE SPECIFIED:

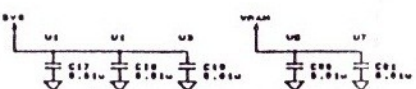
UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY	DATE	ATARI CORPORATION ATARI
ON LINE R	ON HOLES	<i>M. J. ...</i>	<i>8/27/87</i>	
UNDER 30 ±	30 THRU 50 ±	CHECKED	DATE	TITLE
30 THRU 50 ±	OVER 50 ±	<i>K. ...</i>	<i>8/27/87</i>	ASSY MAIN PC0
MATERIAL		APPROVED	DATE	SIZE   DRAWING NO.   REV.
NEXT ASSY		<i>W. S. ...</i>	<i>8/27/87</i>	C   CA200956-XXX   B
USED ON ORIGINAL MODEL		APPROVED	DATE	SCALE NONE SHEET 8 of 8
APPLICATION		<i>S. ...</i>	<i>8/27/87</i>	



REV	REVISIONS DESCRIPTION	DATE	APPROVED
A	PRODUCTION RELEASE	7/10/80	T. DUNN
B	REVISED PER ECO NO. 01007	9/8/80	T. DUNN
C	REVISED PER ECO NO. 01010	11-2-80	<i>[Signature]</i>



IC	BYE	VRAM	DMD
U1	01, 04, 48		00, 01
U2	32	00	10
U3	00	00	10
U4	00	00	10
U5	00	00	10
U6	00	00	10
U7	00	00	10
U8	00	00	10

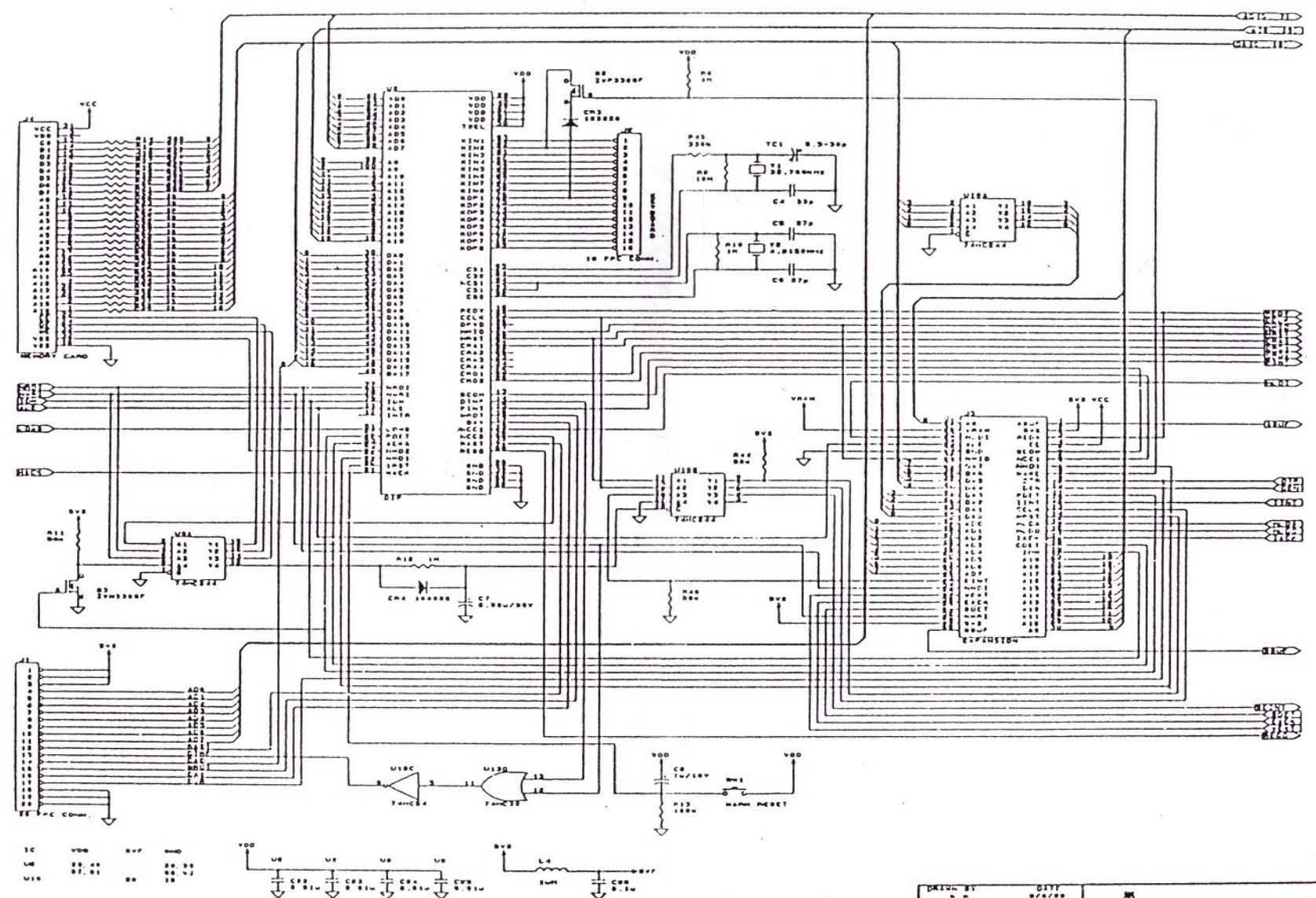


0 CAPACITORS ARE MEASURED IN FARADS.  
 1 RESISTORS ARE MEASURED IN OHMS.  
 NOTES UNLESS OTHERWISE SPECIFIED

DESIGNED BY W. HANNA	DATE 3/1/80	<b>ATARI CORPORATION</b> TITLE <b>SCHEMATIC DIAGRAM</b> CPU PC 6
DRAWN BY K. SHIBAHARA	DATE 3/1/80	
CHECKED BY K. SHIBAHARA	DATE 3/2/80	BYT C
APPROVED BY S. SHIBAHARA	DATE 3/2/80	REV C



REV	REVISION DESCRIPTION	DATE	APPROVED
	SEE SHEET 1		



IC	VDD	VFF	IND
U8	88.41	88	88.42
U10	87.41	88	88.42

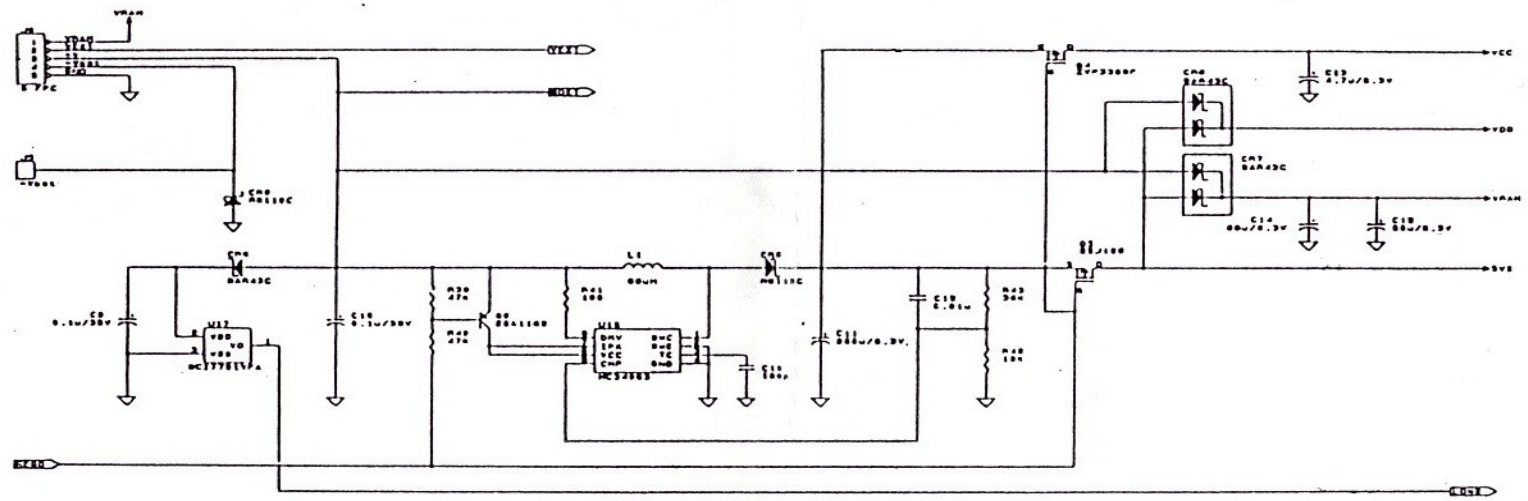
DECOUPLING CAPACITORS FOR IC'S

U8 88.41 88 88.42  
 U10 87.41 88 88.42

NOTES: UNLESS OTHERWISE SPECIFIED

DRAWN BY		DATE	ATARI CORPORATION	
CHECKED		DATE	TITLE	
DESIGNED		DATE	SCHEMATIC DAIGRAM	
APP'D		DATE	CPU PC 01FCCJ	
PARTS		DATE	PART NO	
DRAWN		DATE	C 103944	
CHECKED		DATE	REV B	

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 2		



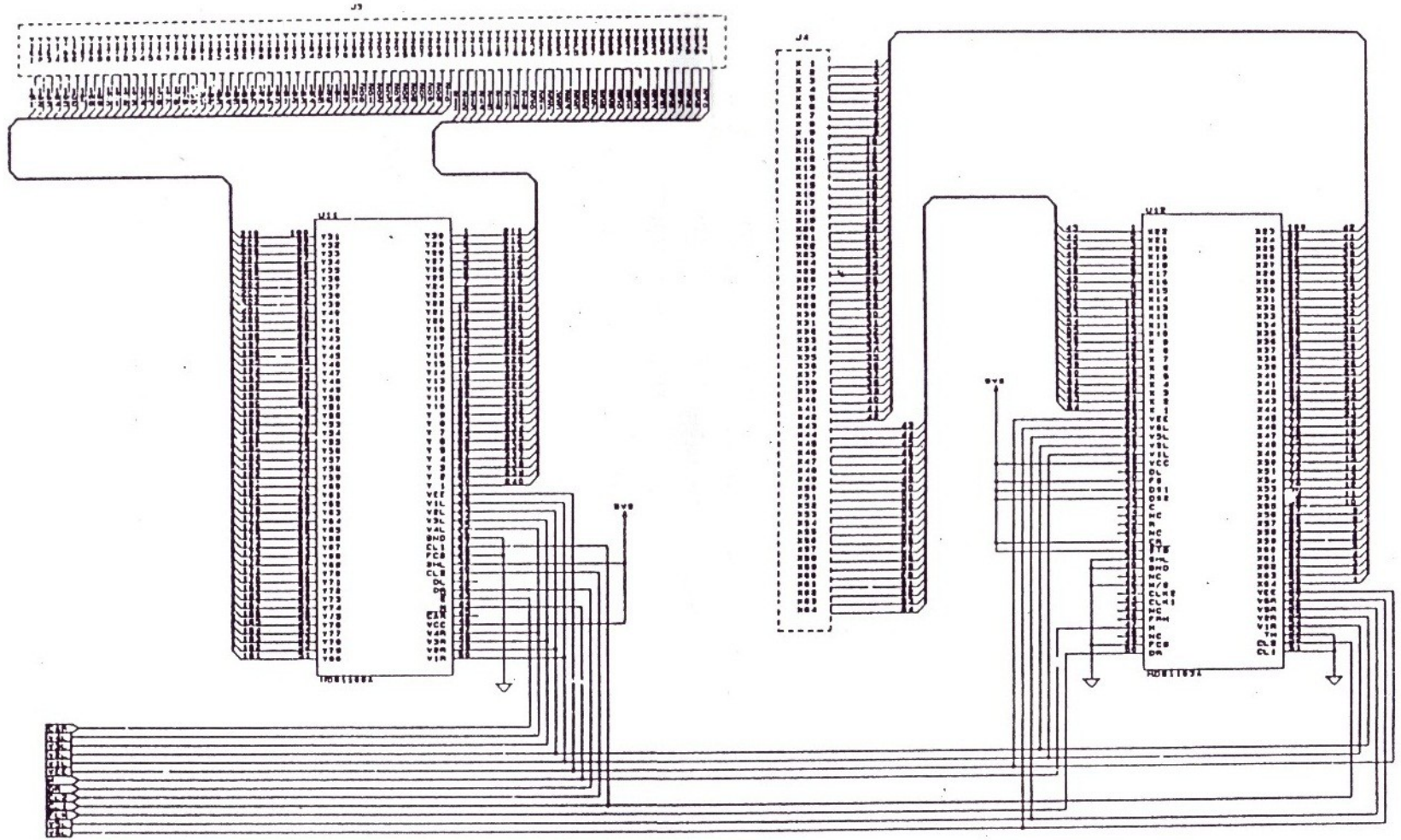
NOTES: ALL DIMENSIONS SPECIFIED

DESIGNED BY	DATE	ATARI CORPORATION
CHECKED BY	DATE	
TESTED BY	DATE	TITLE
LABORATORY	DATE	SCHEMATIC DIAGRAM
DATE	DATE	CPU PC 0 (ECC)
DATE	DATE	REV
DATE	DATE	C
DATE	DATE	C103944





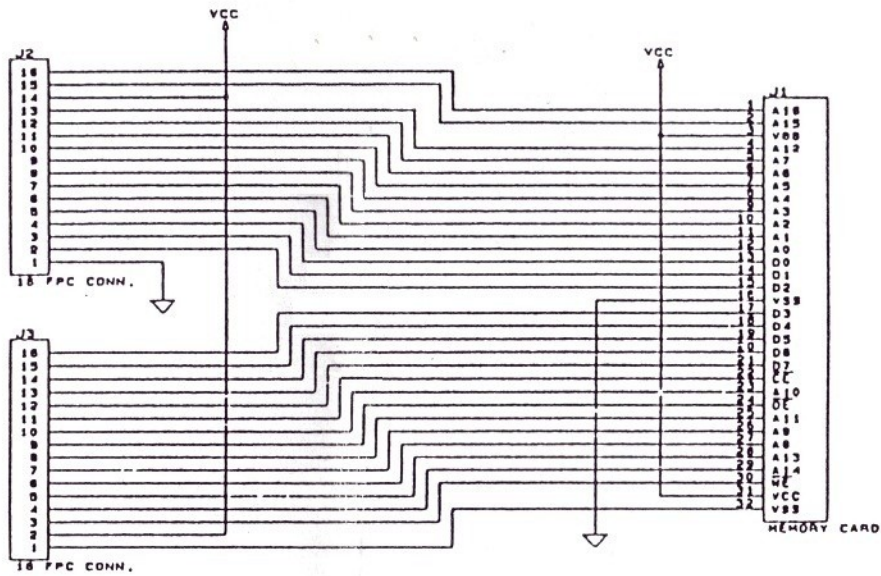





NOTES: UNLESS OTHERWISE SPECIFIED.

DESIGNED BY M. S. M.	DATE 8/2/88	<b>ATARI CORPORATION</b>
DESIGNED BY M. S. M.	DATE 8/2/88	
APPROVED M. S. M.	DATE 8/2/88	<b>TITLE</b> SCHEMATIC DIAGRAM LCD PC 0 (FCC)
DRAWN BY CHECKED BY IN CHARGE APPROVED BY	DATE DATE DATE DATE	<b>DATE DRAWING NO.</b> C 103945
USED IN ORIGINAL MODEL APPROVED BY	DATE	REF. NO. B

REV	REVISIONS DESCRIPTION	DATE	APPROVED
A	PRODUCTION RELEASE	1/21-79	<i>[Signature]</i>



NOTES-UNLESS OTHERWISE SPECIFIED:

DRAWN BY M.M.		DATE 8/2/89	 <b>ATARI CORPORATION</b>
CHECKED <i>J. Tschebik</i>		DATE 8-3-89	
ENGINEER		DATE 8-3-89	TITLE SCHEMATIC DIAGRAM CARD CONNECTOR
APPROVED <i>K. Man...</i>		DATE	SIZE B
CA200584-XXX	FC 0 IMPC-1041	DATE	DRAWING NO. C103860
NEXT ARRY	USED ON ORIGINAL MODEL APPLICATION	APPROVED <i>S. OKAZAKI</i>	DATE 8/1/89
			REV A
			SHEET 1 OF 1